

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant :	Shunpei Yamazaki et al.	Art Unit :	2629
Serial No. :	09/504,235	Examiner :	David L. Lewis
Filed :	February 15, 2000	Confirmation No.:	7610
		Notice of Allowance Date:	March 6, 2008
Title :	METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE		

MAIL STOP ISSUE FEE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

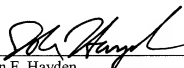
RESPONSE TO NOTICE OF ALLOWANCE

In response to the Notice of Allowance mailed March 6, 2008, enclosed are a completed issue fee transmittal form PTOL-85b. A fee in the amount of \$1400 was paid on February 10, 2005 for the issue fee. The Director of the U.S. Patent & Trademark Office is requested to apply the previously paid fee to the application identified above.

The balance in the amount of \$40 is being paid concurrently herewith on the electronic filing system (EFS) by way of deposit account authorization. Please apply any additional charges or credits to our Deposit Account No. 06-1050.

Respectfully submitted,

Date: June 5, 2008



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